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Docket No.: SON-2939

(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: Toshiyuki Nishihara, et al

Art Unit: 2186

Application No.: 10/798,413

Conf. No. 6020

Filed: March 11, 2004

For: DATA READING METHOD, DATA WRITING

METHOD, AND SEMICONDUCTOR

§1.97(e)(2).

MEMORY DEVICE

SECOND INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Pursuant to 37 CFR §1.56, the attention of the Patent and Trademark Office is hereby directed to the information item(s) listed on the attached PTO-1449. Unless otherwise indicated herein, one copy of each item(s) is attached. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the item(s) be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed (a) within three months of \boxtimes the U.S. filing date or the date of filing a CPA, OR (b) before the mailing date of a first Office Action on the merits in the present application, or (c) accompanies a Request for Continued Examination. No certification or fee is required. This Information Disclosure Statement is being filed more than three months after the U.S. filing date AND after the mailing date of the first Office Action on the merits, but before the mailing date of a Final Rejection or Notice of Allowance. I hereby certify that each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement. 37 CFR §1.97(e)(1). I hereby certify that no item of information in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to my knowledge after making reasonable inquiry, was known to any individual designated in 37 CFR §1.56(c) more than three months prior to the filing of this Information Disclosure Statement. 37 CFR

	c.	A check in the amount of \$180.00 in payment of the fee under 37 CFR §1.17(p). Please charge any fee deficiency or credit any overpayment to Deposit Account No. 18-0013 as needed to ensure consideration of the disclosed information.				
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		a. I hereby certify that each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement. 37 CFR §1.97(e)(1).				
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\boxtimes	4.	The reference(s) was/were cited on pages 43 and 61 of the specification.				
X	5. In the event any fees are due with this paper, please charge our Deposit Accor No. 18-0013.					
		Respectfully submitted,				
Dated	l: April	25, 2005 Ropald P. Kananen				
		Registration No. 24,104				
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Suite						
	_	D.C. 20036				
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Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number. Complete if Known Substitute for form 1449A/B/PTO 10/798,413 Application Number March 12, 2004 INFORMATION DISCLOSURE Filing Date STATEMENT BY APPLICANT Toshiyuki Nishihara First Named Inventor 2186 (Use as many sheets as necessary) Not Yet Assigned Examiner Name SON-2939 1 Attorney Docket Number of 1 Sheet

U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No.1	Document Number Number-Kind Code ² (if known)	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
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	FOREIGN PATENT DOCUMENTS						
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		NON PATENT LITERATURE DOCUMENTS	
Examiner Initials	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, senal, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	CA	2001 Symposium on VLSI Circuit Digest of Technical Papers, "A Bit-Line GND Sense Technique for Low-Voltage Operation FeRAM", Kawashima, et al.	
	СВ	ISSCC 2003 Session 16, "Non-Volatile Memory", Sakimura, et al	↓_
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